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ABSTRACT OF THE DISCLOSURE

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An LC oscillator (I) comprises a cross-coupled PMOS transistor pair (Ma, Mb) coupled to a pair of capacitors (Cva, Cvb) and a pair of inductances (La, Lb). To enhance the signal amplification of the oscillator, a pair of auxiliary transistor circuits (Qa, Qb; Na, Nb) is provided which are coupled between the drain and, preferably, the source of each PMOS transistor. The capacitors (Cva, Cvb) are preferably variable capacitors and the inductances (La, Lb) are preferably connected to ground to allow a enlarged tuning voltage range.